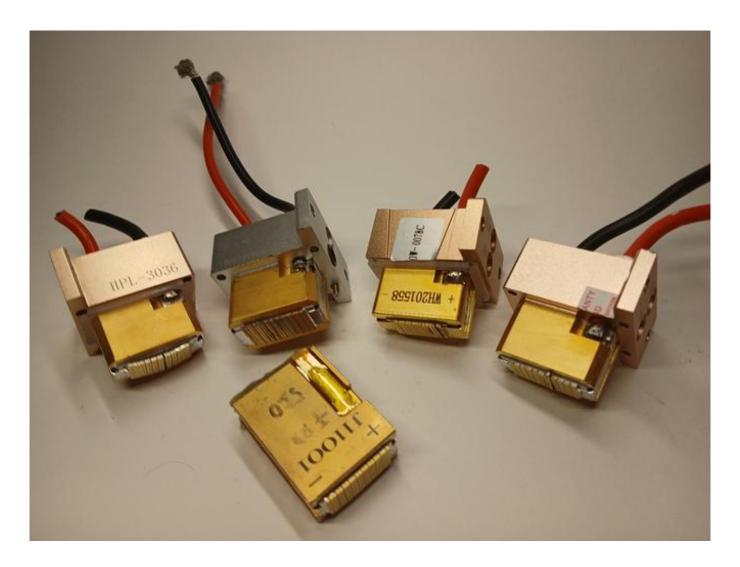
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## WHAT VOLTAGE SIGNAL SHAPE IS EXPECTED IN THE DIODE STACK



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